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Title proper: VLSI design.

Other variant title: Very large scale integration design

Country: United Kingdom

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Last modification date: 06/02/2020

Type of record: Confirmed

ISSN Center responsible of the record: ISSN National Centre for the UK

URL: <https://www.hindawi.com/journals/vlsi/>

SUDOC: <http://www.sudoc.fr/048906743>

Wikidata: <http://www.wikidata.org/entity/Q15763135>

FATCAT: <https://fatcat.wiki/container/dgruxbbpd5bedpkwsg4wfnroya>

Publisher: Hindawi

From: 1993

To: 2018

Keeper: CLOCKSS Archive

Status: In Progress

Extent of archive: 1, 1, 2, 4, 5, 7, 9, 10, 12, 14, 2007 to 2013, 2016 to 2018

Updated: 25/04/2022

Publisher: Hindawi

From: 2014

To: 2015

Keeper: CLOCKSS Archive

Status: Preserved

Extent of archive: 2014, 2015

Updated: 25/04/2022

Publisher: Hindawi

From: 1992

To: 2018

Keeper: LOCKSS Archive

Status: Preserved

Extent of archive: 0 to 2, 4, 5, 7, 9, 10, 12, 14, 2007 to 2011, 2014 to 2018

Updated: 25/04/2022

Publisher: Hindawi

From: empty

To: empty

Keeper: Library of Congress

Status: Preserved

Extent of archive: 2008; 2009; 2010; 2011; 2012; 2013; 2014; 2015; 2016; 2017; 2018

Updated: 08/04/2022

Publisher: Hindawi

From: 1993

To: 2018

Keeper: Portico

Status: Preserved

Extent of archive: Preserved : 1 (1 to 4); 2 (1 to 4); 3 (1, 2, 3-4); 4 (1 to 4); 5 (1 to 4); 6 (1-4); 7 (2 to 4); 8 (1-4); 9 (2 to 4); 10 (1 to 4); 11 (1 to 4); 12 (1 to 4); 13 (1-4); 14 (2 to 4); 15 (1 to 4); 2007 (null); 2008; 2009; 2010; 2011; 2012; 2013; 2014; 2015; 2016; 2017; 2018

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